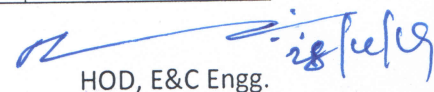


DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NITK- SURATHKAL
SHORT LISTED CANDIDATES FOR WRITTEN TEST & INTERVIEW
FOR PHD PROGRAMME DECEMBER 2019 ADMISSION

The following applicants have been selected for written exam and/or interview for the department for the department of Electronics and Communication Engineering for PhD Programme. The applicants are requested to go through additional information provided in their Call letters sent via e-mail. Syllabus for the written test along with the date and time of written test and interview can be found on the second page of this document.

Sl. No.	Name of the Candidate	Application No.
1.	S V RAVI KUMAR	PH2019EC0002
2.	KRISHNA S S	PH2019EC0004
3.	P RAGHAVENDRA RAO	PH2019EC0007
4.	RAVI KUMAR PALLAVOLU	PH2019EC0008
5.	BINESH K	PH2019EC0010
6.	PONNEKANTI RESHMA	PH2019EC0011
7.	MAHENDRA B V	PH2019EC0012
8.	SANTOSH KUMAR SAHU	PH2019EC0013
9.	BASANT	PH2019EC0015
10.	U. GNANESHWARA CHARY	PH2019EC0016
11.	BIPUL BORO	PH2019EC0017
12.	VEDANTHAM PRAVEEN KUMAR	PH2019EC0019
13.	SAVIDHAN SHETTY C S	PH2019EC0020
14.	RAMACHANDRA H N	PH2019EC0021
15.	RICHU PAUL	PH2019EC0022
16.	KARUTHARAJA V	PH2019EC0023
17.	MALLIKARJUN DHESHMUK	PH2019EC0024
18.	AMIYA KUMAR MONDAL	PH2019EC0025
19.	SHASHIDHARA M	PH2019EC0026
20.	ABHISHEK KUMAR	PH2019EC0028
21.	PAMARTHI UMA SINDHU	PH2019EC0029
22.	SREENIVASA T V	PH2019EC0030
23.	SURAJ S	PH2019EC0031
24.	VISHWANATH SHIDLINGAPPANAVAR	PH2019EC0032
25.	YALAGALA SIVANJANEYULU	PH2019EC0034
26.	MUTHAMALA RAVI	PH2019EC0035
27.	LAVANYA L	PH2019EC0036


HOD, E&C Engg.

प्राध्यापक एवं विभागाध्यक्ष / PROF & HEAD
डी एवं सी विभाग / E & C Department
एन आई टी के, सुरतकल/NITK, Surathkal
मंगलूर / MANGALORE - 575 025

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NITK- SURATHKAL**

Syllabus for Ph.D. admission test.

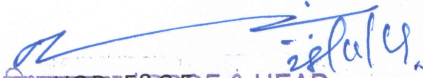
The test paper will have 3 parts. Part A is compulsory. The candidate can answer Part B or Part C depending on the area of interest. Each part has 15 multiple choice type questions. Each correct answer carries 1 mark and wrong answer carries – 0.25 marks.

Part A: Linear Algebra, Calculus, Differential and Difference equations. Numerical methods, Transforms, Linear circuits and networks, Electronic components and Devices, Analog Electronics, Digital Electronics, Signals and Systems, Linear and Digital Control Theory.

Part B: Electromagnetic Waves, Probability and Random Processes, Communication Theory, Communication Circuits, Transmission Lines, Wave Guides, Antennas, Microwave devices and Circuits, Data Communications, Communication Networks, Satellite Communication, Optical Communication, Signal Processing.

Part C: Linear and Digital IC's, Digital System Design, VLSI Technology, CMOS, VLSI, Mixed Signal Design, HDL, Data converters, Microprocessors, Computer Architecture and organization, Logic Synthesis, DSP Architectures, Embedded Systems.

Place of Reporting	: Dept. of Electronics and Communication Engg.
Written Test	: 12 th December, 2019, 9.00 AM – 10 AM
Document Verification	: 12 th December, 10.15 AM onwards
Interview	: 12 th December, 11 AM onwards


प्राध्यापक एवं विभागाध्यक्ष, E&C Engg. HEAD
ई एवं सी विभाग / E & C Department (ilk)
एन आई टी के, सुराथकल/NITK, Surathkal
मंगलूर / MANGALORE - 575 025