NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING POST SRINIVASNAGAR, MANGALORE – 575 025 (D K)

Phone: (0824) 2474000. Fax: (0824) 2474033 **E- mail: info@nitk.ac.in Website: http://www.nitk.ac.in**



NOTICE INVITING QUOTATION

Notification. No: NITK/EC/OH35-IRG/Q/970/2023 dated: 09/01/2023

Name of Goods	Digital Logic Analyzer
Estimated Amount:	₹ 1.25 Lakhs
Time for Supply of item after release of Purchase order	20 Days
Document Download / Sale Start Date	09/01/2023 at 6.00 PM
Clarification Start Date	N/A
Clarification End Date	N/A
Bid Submission Start Date	09/01/2023 at 6.00 PM
Last Date for submission of bids	19/01/2023 before 3.00 PM
Bid Opening Date	19/01/2023 at 4.00 PM
Address for Submission of bids	Head of the Department, Electronics & Communication Engg.
Address of the Department and Contact Person	Srinivasnagar Post, NITK Surathkal. Pincode-575025, D.K
Details	Kind Attension : Dr. Deepu Vijayasenan
	Mob: 9739327595, Tel: 0824-2473046



NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

POST SRINIVASNAGAR, MANGALORE - 575 025

dated: 09/01/2023

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NOTICE INVITING QUOTATION (NIQ)

The National Institute of Technology Karnataka, Surathkal (in short – NITK, Surathkal) is an Institute Of National Importance Under Ministry of Education Govt of India, imparting Technical Education and engaged in Research Activities. It is proposed to procure the items for the departmental academic/research activities.

Sealed Quotations as per the Price Schedule given in this NIQ are invited for the following items subject to the terms and conditions, from the reputed manufacturers or its authorised dealers so as to reach on or before scheduled date and time. The quotations in the firm's Business letter head should be address to the "Director, NITK, Surathkal". The envelope shall be superscribed with the Quotation Notification Number and the Name of the Goods for which quotation is submitted.

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Sd/-[HOD]

Note: Institute shall not be responsible for any postal delay about non-receipt /non-delivery of the bids or due to wrong addressee.

SECTION-1 Terms and Conditions

- 1. The rates should be quoted for preferably FOR destination from supply within India.
- 2. The bidder shall indicate the excise duty exemption for the goods if applicable.
- The rate quoted should be on unit basis. Taxes and other charges should be quoted separately, considering exemptions if any. The rate should be quoted in INR only
- 4. Rate quoted should be inclusive of Testing, commissioning and Installation of equipment and Training.
- 5. Payment: No advance payment will be made. Payment will be made only after the supply of the item in good and satisfactory condition and receipt of performance security by supplier.
- 6. Guarantee/Warrantee period should be specified for the complete period should be specified in section 3 of this tender document.
- 7. Period requirement for the supply and installation of item should be specified in section 3 of this tender document.
- 8. In case of dispute, the matter will be subject to Mangalore Jurisdiction only.

SCHEDULE OF REQUIREMENTS, SPECIFICATIONS AND ALLIED DETAILS

[To be filled up by the Department / Centre of NITK, Surathkal]

Item(s) Name to be Procured : **Digital Logic Analyzer**

Brief Specifications of the Item(s)

(Attach Additional Sheet if necessary)

: Attached

Quantity : 2 Nos.

Any other details / requirement : N/A

Warranty Period required : ≥ 2 years

Delivery Schedule expected

after placement of Purchase order

(in Weeks) : 3 Weeks

SECTION 3 PRICE SCHEDULE

[To be used by the bidder for submission of the quotation]

1.	Item Name	:		
2.	Specifications (Conforming to Schedule of requirements Enclose additional sheets if necessary)	:		
3.	Currency and Unit Price	:		
4.	Quantity	:		
5.	Item Cost (SI No. 3 * SI. No. 4)	:		
6.	Taxes and Other Charges (i) Specify the type of taxes and duties in percentages and also in figures. (ii) Specify Other Charges in figures.	:		
7.	Warranty Period (Conforming to the Schedule of requirements)	:		
8.	Delivery Schedule (Conforming to the Schedule of requirements)	:		
9.	Name and address of the Firm for placing purchase order	:		
10.	Name and address of Indian authorized agent (in case of imports only)	:		
Signature of the Bidder:				
Name	and Designation:			
Business Address :				
Place: Date:		Seal of the Bidder	's Firm	

SECTION 4 CONTRACT FORM

[To be provided by the bidder in the business letter head]

- 1. (Name of the Supplier's Firm) hereby abide by the delivery schedule mentioned in this document for supply of the items if the purchase order is awarded.
- 2. The item will be supplied conforming to the specifications stated in this document without any defect and deviations.
- 3. Warranty will be given for the period mentioned in this document and service will be rendered to the satisfaction of NITK, Surathkal during this period.

Signature of the Bidd	er:	
Name	:	
Business Address	:	
Place : Date :		Seal of the Bidder's Firm

Specification for Digital Logic Analyzer

Detailed Specifications:

Quantity: 2 Nos.

Feature	Description
Channels	8 digital channels, 8 analog inputs (shared with digital channels)
Max sample	Digital: 500 MSPS @ 4 channels, 100 MSPS @ 8 channels (USB 3.0 required)
rates	Analog: 50 MSPS @ 3 channels, 12.5 MSPS @ 8 channels (USB 3.0 required)
Bandwidth	Digital: 100 MHz square wave when sampling at 500 MSPS
	Analog: 5 MHz when sampling at 50 MSPS
Trigger	Edge or pulse width trigger
Software	Cross platform Windows, Mac and Linux support
Protocols	Serial, SPI, I2C, Atmel SWI, Biss-C, DMX-512, HD44780, HDLC, I2S, JTAG, LIN,
support	MDIO, MIDI, Manchester, Modbus, 1-Wire, PS/2 Keyboard & Mouse, SMBUs, SWD,
	Synchronous Parallel, USB, UNI/O
Capture Buffer	Limited by installed memory and digital data density. When recording analog at 50
Length	MSPS, captures 10-60 seconds usually possible.
	Without analog, buffer length is dependent on digital activity density. 1 MHz
	SPI: up to 10 mins, I2C: several hours- 9600 baud serial- over 1 day
Analog	12 bits, 4.88 mv per LSB
resolution	
Analog input	-10 V to 10 V
range	
Digital IO	1.2 V, 1.8 V, 2.5 V, 3.3 V, 5 V, RS 232, +12 V TTL
standards	
support	

Warranty Period: ≥ 2 Years