



IRIS

NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL

राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक, सुरत्कल

P.O SRINIVASNAGAR, MANGALORE - 575025

**EC Call Letter List**

M Tech (Self-Financed), 2024 - 25

The following applicants have been selected for written exam and/or interview for the department for the department of Electronics and Communication Engineering for M Tech (Self-Financed) Programme. The applicants are requested to go through additional information provided in their Call letters.

#	Name	Reference Number	Branch/Specialisation
1	Nagaraj	MT2024VL0002	VLSI Design
2	Nagaraj	MT2024SP0002	Signal Processing and Machine Learning
3	Dipanjan Dutta	MT2024VL0003	VLSI Design
4	Nitila Gokulkrishnan	MT2024SP0004	Signal Processing and Machine Learning
5	Abu Saleh Khan	MT2024VL0004	VLSI Design
6	Abu Saleh Khan	MT2024CN0003	Communication Engineering and Network
7	POTHULAPALLY HARSHAVARDHAN	MT2024VL0005	VLSI Design
8	Ashwin M	MT2024VL0007	VLSI Design
9	Nitheesh Kumar	MT2024VL0008	VLSI Design
10	MANJUNATH H S	MT2024VL0009	VLSI Design
11	Shruchi Jha	MT2024VL0010	VLSI Design
12	Rajkumar Meshram	MT2024VL0012	VLSI Design
13	Padmanabhan Rajesh Nair	MT2024VL0013	VLSI Design
14	RASAKONDA PRERNA	MT2024VL0014	VLSI Design
15	SAGNIK DHAR	MT2024VL0015	VLSI Design
16	SAGNIK DHAR	MT2024SP0006	Signal Processing and Machine Learning

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25.6.2024



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#	Name	Reference Number	Branch/Specialisation
17	Vandana chowdari	MT2024CN0005	Communication Engineering and Network
18	Vandana chowdari	MT2024SP0007	Signal Processing and Machine Learning
19	Vandana chowdari	MT2024VL0016	VLSI Design
20	SHAIK KHALEEL	MT2024VL0017	VLSI Design
21	Pravallikaa M	MT2024VL0018	VLSI Design
22	Pravallikaa M	MT2024SP0008	Signal Processing and Machine Learning
23	Pravallikaa M	MT2024CN0006	Communication Engineering and Network
24	DIBYAJIT SWAIN	MT2024VL0019	VLSI Design
25	ANIKET SOHAN BISOI	MT2024VL0020	VLSI Design
26	ANIKET SOHAN BISOI	MT2024SP0009	Signal Processing and Machine Learning
27	Arun Vaishnav	MT2024VL0021	VLSI Design
28	PA SAISIVAKUMARAN	MT2024VL0022	VLSI Design
29	MOKESH KUMAR V	MT2024VL0023	VLSI Design
30	GIRI VEDANT ASHOK	MT2024VL0024	VLSI Design
31	priya singh	MT2024VL0026	VLSI Design
32	AAKASH CHATURVEDI	MT2024VL0027	VLSI Design
33	AAKASH CHATURVEDI	MT2024SP0012	Signal Processing and Machine Learning
34	AAKASH CHATURVEDI	MT2024CN0008	Communication Engineering and Network
35	Ananthakrishnan L	MT2024VL0028	VLSI Design
36	Surya G	MT2024VL0029	VLSI Design

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#	Name	Reference Number	Branch/Specialisation
37	SUDEV PATEL U	MT2024VL0030	VLSI Design
38	SUDEV PATEL U	MT2024SP0014	Signal Processing and Machine Learning
39	Shreyas V Devadiga	MT2024VL0032	VLSI Design
40	RADHIKA RAGHAV	MT2024VL0033	VLSI Design
41	RADHIKA RAGHAV	MT2024CN0010	Communication Engineering and Network
42	RADHIKA RAGHAV	MT2024SP0016	Signal Processing and Machine Learning
43	Arushi Saxena	MT2024VL0034	VLSI Design
44	BISHAL BHATTACHARJEE	MT2024VL0035	VLSI Design
45	Arushi Saxena	MT2024SP0017	Signal Processing and Machine Learning
46	Shreesh Dube	MT2024VL0036	VLSI Design
47	YETRA SRIMANNARAYANA REDDY	MT2024VL0037	VLSI Design
48	Argha Nath	MT2024SP0019	Signal Processing and Machine Learning
49	AAKASH CHATURVEDI	MT2024VL0038	VLSI Design
50	AAKASH CHATURVEDI	MT2024CN0011	Communication Engineering and Network
51	AAKASH CHATURVEDI	MT2024SP0020	Signal Processing and Machine Learning
52	Varshith R	MT2024VL0039	VLSI Design
53	Vivek V	MT2024VL0040	VLSI Design
54	ADITI K UTTARKAR	MT2024VL0041	VLSI Design
55	nipun sharma	MT2024VL0043	VLSI Design
56	Gokul S R	MT2024VL0044	VLSI Design


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#	Name	Reference Number	Branch/Specialisation
57	Marri Saketh Kumar	MT2024VL0045	VLSI Design
58	Anusha K R	MT2024SP0021	Signal Processing and Machine Learning
59	Amal Suresh	MT2024SP0023	Signal Processing and Machine Learning
60	SUDAN M	MT2024VL0046	VLSI Design
61	Ganesan Sai Krishnan	MT2024VL0047	VLSI Design
62	RAJEEV RANJAN	MT2024VL0048	VLSI Design
63	Anusha K R	MT2024VL0049	VLSI Design
64	JAYANTH ADUSUMILLI	MT2024VL0050	VLSI Design
65	PANYAMPRAVALLIKA	MT2024CN0012	Communication Engineering and Network
66	potnuru jai shankar	MT2024CN0013	Communication Engineering and Network
67	ROSHAN SINGH	MT2024VL0051	VLSI Design
68	ANUPAMA R	MT2024VL0052	VLSI Design
69	Shubham Sanjay Chachan	MT2024VL0053	VLSI Design
70	Shubham Sanjay Chachan	MT2024SP0025	Signal Processing and Machine Learning
71	Shubham Sanjay Chachan	MT2024CN0014	Communication Engineering and Network
72	Animesh Sharma	MT2024VL0054	VLSI Design
73	JOEL M JACOB	MT2024VL0056	VLSI Design
74	K V Diya Murali	MT2024VL0057	VLSI Design
75	Rojivadia Bhumitkumar Ratilal	MT2024VL0058	VLSI Design
76	MADANA VICTOR VASANTH	MT2024VL0059	VLSI Design
77	MADANA VICTOR VASANTH	MT2024SP0028	Signal Processing and Machine Learning

  
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78 #	BHARGAVI S JOSHI Name	Reference Number	VLSI Design Branch/Specialisation
79	BHARGAVI S JOSHI	MT2024VL0060	Signal Processing and Machine Learning
80	BHARGAVI S JOSHI	MT2024SP0029	Communication Engineering and Network
81	Vishwanath S	MT2024CN0015	VLSI Design
82	RAJSREE S	MT2024VL0062	VLSI Design
83	Shaun Sunil	MT2024VL0063	VLSI Design
84	Devarakonda Sri Deepthi	MT2024VL0064	Signal Processing and Machine Learning
85	SEIKH HUSNINE	MT2024SP0030	VLSI Design
86	Soumyadeep Bhunia	MT2024VL0065	VLSI Design
87	ABHIRAM K M	MT2024VL0066	VLSI Design
88	GOVIND M NAIR	MT2024VL0067	VLSI Design
89	ANURADHA KUMARI	MT2024VL0068	VLSI Design
90	Jachin Bency Thomas	MT2024VL0069	VLSI Design
91	RA SRINETHE	MT2024VL0070	VLSI Design
92	RA SRINETHE	MT2024VL0072	Signal Processing and Machine Learning
93	RA SRINETHE	MT2024SP0031	Communication Engineering and Network
94	Karthick-R	MT2024CN0018	Communication Engineering and Network
95	Soumyadeep Bhunia	MT2024CN0019	Signal Processing and Machine Learning
96	LEETHU M THOMAS	MT2024SP0032	VLSI Design
97	RAJEEV RANJAN	MT2024VL0073	Signal Processing and Machine Learning

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M Tech (Self-Financed), 2024 - 25

#	Name	Reference Number	Learning Branch/Specialisation
98	AHILAN R	MT2024VL0074	VLSI Design
99	Ankush Balu Dhomne	MT2024VL0075	VLSI Design
100	Narayandas Sai Kiran	MT2024VL0076	VLSI Design
101	Narayandas Sai Kiran	MT2024SP0034	Signal Processing and Machine Learning
102	Mohak Singh	MT2024VL0077	VLSI Design
103	Mohak Singh	MT2024CN0020	Communication Engineering and Network
104	ZAILESH A R	MT2024VL0078	VLSI Design
105	Subhro Choudhury	MT2024CN0021	Communication Engineering and Network
106	SIVAPRASAD P T	MT2024VL0079	VLSI Design
107	GARIMA SINGH	MT2024VL0080	VLSI Design
108	Abhishek	MT2024VL0081	VLSI Design
109	Ankita Goswami	MT2024VL0082	VLSI Design
110	ANURADHA KUMARI	MT2024CN0023	Communication Engineering and Network
111	Chandrani Kakoty	MT2024VL0083	VLSI Design
112	SHEIKH MUJAHID UL ISLAM HAMZA	MT2024VL0084	VLSI Design
113	Tigunait Anchal Avdhesh	MT2024VL0085	VLSI Design
114	Rojivadia Bhumitkumar Ratilal	MT2024VL0086	VLSI Design
115	Rojivadia Bhumitkumar Ratilal	MT2024SP0036	Signal Processing and Machine Learning
116	Pranav Sagar	MT2024VL0087	VLSI Design
117	Pranav Sagar	MT2024SP0037	Signal Processing and Machine Learning

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#	Name	Reference Number	Branch/Specialisation
118	Aryan Shrivastava	MT2024VL0088	VLSI Design
119	Aswan	MT2024VL0090	VLSI Design
120	SANGEETA SHARMA	MT2024VL0091	VLSI Design
121	Patil Aditya Appasaheb	MT2024CN0026	Communication Engineering and Network
122	Patil Aditya Appasaheb	MT2024VL0092	VLSI Design
123	Vivek V	MT2024SP0038	Signal Processing and Machine Learning
124	DHULIPALLA VENU	MT2024VL0093	VLSI Design
125	shahna	MT2024SP0039	Signal Processing and Machine Learning
126	RAVULA LATHASREE	MT2024VL0094	VLSI Design
127	RAVULA LATHASREE	MT2024SP0040	Signal Processing and Machine Learning
128	Pranav Sagar	MT2024CN0027	Communication Engineering and Network
129	kadali sai sri ram	MT2024VL0097	VLSI Design
130	Rishabh Kumar Gupta	MT2024VL0098	VLSI Design
131	Vaibhav Vyas	MT2024SP0041	Signal Processing and Machine Learning
132	ARUNABHA SARKAR	MT2024VL0099	VLSI Design
133	AVINASH KUMAR MISHRA	MT2024VL0100	VLSI Design
134	AVINASH KUMAR MISHRA	MT2024SP0043	Signal Processing and Machine Learning
135	SHAIK RAHIMAN	MT2024VL0102	VLSI Design
136	Surya G	MT2024VL0103	VLSI Design
137	NEETU KUMARI	MT2024VL0104	VLSI Design
138	shakshi priya	MT2024VL0105	VLSI Design

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#	Name	Reference Number	VLSI Design Branch/Specialisation
139	DEMULA BHANU PRAKASH	MT2024VL0106	VLSI Design
140	DEMULA BHANU PRAKASH	MT2024CN0028	Communication Engineering and Network
141	DEMULA BHANU PRAKASH	MT2024SP0044	Signal Processing and Machine Learning
142	shakshi priya	MT2024SP0045	Signal Processing and Machine Learning
143	Cheedella Ram Sai Praneeth	MT2024VL0107	VLSI Design
144	ASHIN V A	MT2024VL0108	VLSI Design
145	Parthapratim Baishya	MT2024VL0109	VLSI Design
146	Madhav Prasad P T	MT2024VL0110	VLSI Design
147	Madhav Prasad P T	MT2024CN0029	Communication Engineering and Network
148	MOHAMMED ZUNAID KHAN NAYAK	MT2024VL0111	VLSI Design
149	HARSH ANAND	MT2024VL0112	VLSI Design
150	Apeksha Shekar	MT2024VL0113	VLSI Design
151	Prajwal Hiremath	MT2024VL0115	VLSI Design
152	Prajwal Hiremath	MT2024SP0047	Signal Processing and Machine Learning
153	Soumik Chakraborty	MT2024VL0116	VLSI Design
154	Anuvrat Milind Marathe	MT2024VL0117	VLSI Design
155	Anuvrat Milind Marathe	MT2024CN0030	Communication Engineering and Network
156	Reetam Ranjan	MT2024VL0118	VLSI Design
157	Reetam Ranjan	MT2024SP0049	Signal Processing and Machine Learning
158	SOUMALYA BISWAS	MT2024VL0119	VLSI Design

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#	Name	Reference Number	Branch/Specialisation
159	Satwik Pal	MT2024VL0121	VLSI Design
160	Amritangsu sen	MT2024VL0122	VLSI Design
161	SOUMALYA BISWAS	MT2024SP0051	Signal Processing and Machine Learning
162	Morthala Kranthi Kumar Reddy	MT2024VL0123	VLSI Design
163	Morthala Kranthi Kumar Reddy	MT2024SP0053	Signal Processing and Machine Learning
164	Morthala Kranthi Kumar Reddy	MT2024CN0032	Communication Engineering and Network
165	SOURABH JOSHI	MT2024VL0124	VLSI Design
166	Tanaya Mehta	MT2024VL0126	VLSI Design
167	AAKARSH K M	MT2024VL0127	VLSI Design
168	BHANU PRATAP	MT2024VL0129	VLSI Design
169	BHANU PRATAP	MT2024CN0033	Communication Engineering and Network
170	Morishetty Balaji	MT2024VL0130	VLSI Design
171	KONATHAM.PRAVEEN KUMAR	MT2024VL0131	VLSI Design
172	AYUSHI CHOUDHARY	MT2024VL0132	VLSI Design
173	Priya katre	MT2024VL0133	VLSI Design
174	Pranav Dhumal	MT2024VL0134	VLSI Design
175	GOPICHAND PRATHIPATI	MT2024VL0135	VLSI Design
176	GOPICHAND PRATHIPATI	MT2024SP0054	Signal Processing and Machine Learning
177	Shardul Shyamsundar Penurkar	MT2024VL0136	VLSI Design
178	Rima Shaw	MT2024VL0138	VLSI Design
179	K Preksha Bhat	MT2024VL0139	VLSI Design

  
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#	Name	Reference Number	Branch/Specialisation
180	AYUSH KUMAR	MT2024VL0140	VLSI Design
181	NAVEEN GURUGUBELLI	MT2024SP0055	Signal Processing and Machine Learning
182	NAVEEN GURUGUBELLI	MT2024VL0141	VLSI Design
183	Madagoni Bharadwaj	MT2024VL0142	VLSI Design
184	Pooja Mahadev Nagali	MT2024VL0143	VLSI Design
185	Avila Lobo	MT2024VL0144	VLSI Design
186	Rohit Aich	MT2024VL0145	VLSI Design
187	Milan P Manoj	MT2024VL0146	VLSI Design
188	Reshma S	MT2024VL0147	VLSI Design
189	NAGWEKAR MAYURESH JAYPRAKASH JUILI	MT2024VL0148	VLSI Design
190	Sai Ramya	MT2024VL0150	VLSI Design
191	Harshit Ravi Kumar Ambati	MT2024VL0152	VLSI Design
192	Anjana P Sekhar	MT2024VL0153	VLSI Design
193	Anjana P Sekhar	MT2024CN0039	Communication Engineering and Network
194	Anjana P Sekhar	MT2024SP0059	Signal Processing and Machine Learning
195	Gowripriya P	MT2024VL0154	VLSI Design
196	SETHU VEERENDRA MALIREDDI	MT2024VL0155	VLSI Design
197	N Nagamallishwar	MT2024VL0156	VLSI Design
198	Shivani Mishra	MT2024VL0157	VLSI Design
199	Tanmoy Dey	MT2024VL0159	VLSI Design
200	Darisi Abhinay	MT2024VL0160	VLSI Design
201	Gururaj Deepak Todurkar	MT2024VL0161	VLSI Design

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#	Name	Reference Number	Branch/Specialisation
202	Dinesh Venkat G	MT2024VL0162	VLSI Design
203	Dinesh Venkat G	MT2024SP0062	Signal Processing and Machine Learning
204	Shristi Chandrakar	MT2024SP0062	Signal Processing and Machine Learning
205	Shristi Chandrakar	MT2024VL0163	VLSI Design
206	Sai Ramya	MT2024SP0064	Signal Processing and Machine Learning
207	Sai Ramya	MT2024CN0040	Communication Engineering and Network
208	Daneshwari Koshti	MT2024VL0164	VLSI Design
209	DISHA MITRA	MT2024VL0165	VLSI Design
210	S MAYUR KAMATH	MT2024VL0166	VLSI Design
211	Jayaraj S	MT2024VL0167	VLSI Design
212	Dattathreya Pai	MT2024VL0168	VLSI Design
213	Dattathreya Pai	MT2024CN0041	Communication Engineering and Network
214	Pavan Kumar	MT2024VL0169	VLSI Design
215	J S SWAROOP	MT2024VL0170	VLSI Design
216	Ninad Shetty	MT2024VL0171	VLSI Design
217	Triston Sheldon Cutinho	MT2024VL0172	VLSI Design
218	MANISHA KUMARI	MT2024VL0173	VLSI Design
219	Kiran Pojary	MT2024VL0174	VLSI Design
220	Shashanka Handique	MT2024VL0175	VLSI Design
221	Vishwa Deepak Singh	MT2024VL0176	VLSI Design
222	Kartik Saini	MT2024VL0177	VLSI Design

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NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA, SURATHKAL

राष्ट्रीय प्रौद्योगिकी संस्थान कर्नाटक, सुरत्कल

P.O SRINIVASNAGAR, MANGALORE - 575025

EC Call Letter List

M Tech (Self-Financed), 2024 - 25

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25.6.2024

Head Of Department

Electronics and Communication Engineering

प्रोफेसर एवं विभागाध्यक्ष  
इ. एवं सी. विभाग / Department of E & C  
एन.आई.टी.के. सुरत्कल/NITK Surathkal  
मंगलूरु / MANGALURU - 575 025

Dean Academics

Date: 26-06-2024

## Syllabus for M. Tech. (Self-financed) Aptitude Test July 2024

The Test paper has 2 Parts: **Part-1 is compulsory**, and Part-2 is stream specific modules. The candidate is supposed to attempt Module A or Module B or Module C from Part-2 depending on the candidature for a particular M.Tech (Self-financed) streams. Part-1 has 15 multiple choice questions (MCQs), whereas, each module of Part-2 has 5 MCQs. Each correct answer carries 2 marks and wrong answer carries -0.5 marks.

Time duration:

Part-1 : 30 Minutes.

Part-2 : Each module of 10 minutes.

**Scientific calculator is permitted. Mobile phones are strictly prohibited.**

### Part-1 (Compulsory):

Linear Algebra, Calculus, Differential and Difference equations, Numerical methods, Transforms, Linear circuits and networks, Electronic components and Devices, Analog Electronics, Digital Electronics, Signals and Systems, Linear and Digital Control Theory.

### Part-2 (Stream Specific):

#### Module-A (CEN Stream)

Electromagnetic Waves, Probability and Random Processes, Communication Theory, Communication Circuits, Transmission Lines, Wave Guides, Antennas, Microwave devices and Circuits, Data Communications, Communication Networks, Satellite Communication, Optical Communication, Fundamentals of Signal Processing.

#### Module-B (SPML Stream)

**SP Fundamentals:** Time domain analysis of discrete-time systems - Basic discrete time signals, discrete-time Fourier Series, Z Transform – definition and properties, Discrete-time Fourier Series and its properties, Properties and applications of DTFT. Relationship between time, Z and frequency domains, DFT fundamentals and Properties, Design and applications of FIR and IIR filters.

**Data Structure Fundamentals:** Algorithm analysis, Asymptotic notations. Divide and Conquer algorithms. Analysis of divide and conquer algorithms & examples, Merge sort, quick sort, binary search, Data structures, Linked list, stacks and queues.

#### Module-C (VLSI Design Stream)

Linear and Digital ICs, Digital System Design, VLSI Technology, CMOS VLSI, Mixed Signal Design, HDL, Data converters, Microprocessors, Computer Architecture and organization, Logic Synthesis, DSP Architectures, Embedded Systems.

अध्यक्ष एवं विभागाध्यक्ष/PROF & HEAD  
एवं सी. विभाग / Department of E & C  
एन.आई.टी.के. सुरथकल/NITK Surathkal  
26-6-2024

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA SURATHKAL**

**P.O. SRINIVASNAGAR, MANGALURU-575 025  
Telephone: 0824-2473046, Website: www.ece.nitk.ac.in**

**Date: 26-06-2024**

**Shortlisted candidates for M. Tech. self-financed programme–Written Aptitude Test & Interview for the  
Year 2024-25**

With reference to your application for admission to M. Tech. self-financed programme in the department of Electronics and Communication Engineering, you are requested to appear for the Written test and Interview at ECE Dept., NITK Surathkal.

You should produce all the original records such as Date of Birth Certificate, GATE Score Card (those who have), Degree Certificate and Marks Cards of all semesters (UG programme), SC/ST/OBC/EWS certificate (if applicable as per proforma), Person with disability certificate (if applicable), Conduct Certificate and valid photo identity card. Please keep a self-attested photocopy of all these certificates readily available at the time of document verification.

Department	: Department of Electronics and Communication Engineering
Place of Reporting	: Department of Electronics and Communication Engineering
Document Verification	: July 03, 2024, 9.00 AM onwards, Analog Electronics Lab, ECE Department
Written Test	: July 03, 2024 2.00 PM at ECE Department
Technical Interview	: 9.00 AM Onwards July 4 <sup>th</sup> , 5 <sup>th</sup> 2024, Meeting room, ECE Department

**NOTE:**

1. Candidates should be prepared to appear for the written Aptitude Test before the technical interview. Fee Structure for M-Tech self-financed programme and Course syllabus are provided on Institute's website. i.e. [www.nitk.ac.in](http://www.nitk.ac.in). A copy of the syllabus for the aptitude test is appended with shortlisted candidates.
2. Candidates who have not submitted marks of final examination along with application form shall produce the same at the time of admission.
3. Your candidature for this test is provisional & is subject to your fulfilling the educational qualifications & other criteria prescribed for the programme as mentioned in the Information Brochure, failing which your candidature can be summarily rejected after verification/scrutiny at a later stage.
4. **Please keep the Admit Card (Hard copy) ready during the offline test and interview.** You are responsible for safe custody of the Admit Card and in the event of any other person using this Admit Card, the responsibility lies on you to prove that you have not used the service of an impersonator.
5. Please note that no expenses shall be payable for appearing in the written test and Interview.
6. Mobile phones are not allowed inside the exam hall. Candidates can use scientific calculator.

  
26.06.2024  
1/Head of the Department

अध्यक्ष एवं विभागाध्यक्ष/PROF & HEAD  
एवं सी. विभाग / Department of E & C  
एन.आई.टी.के. सुरत्कल/NITK Surathkal  
मंगलूर / MANGALURU - 575 025